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SERIAL NUMBER 10/766,013	FILING OR 371(c) DATE 01/29/2004 RULE	CLASS 257	GROUP ART UNIT 2826	ATTORNEY DOCKET NO. 248267US2	
APPLICANTS Hiroki Shinkawata, Tokyo, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2003-044155 02/21/2003 JAPAN 2003-345633 10/03/2003 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/12/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 2
ADDRESS 22850					
TITLE Semiconductor device having memory and logic devices with reduced resistance and leakage current					
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		